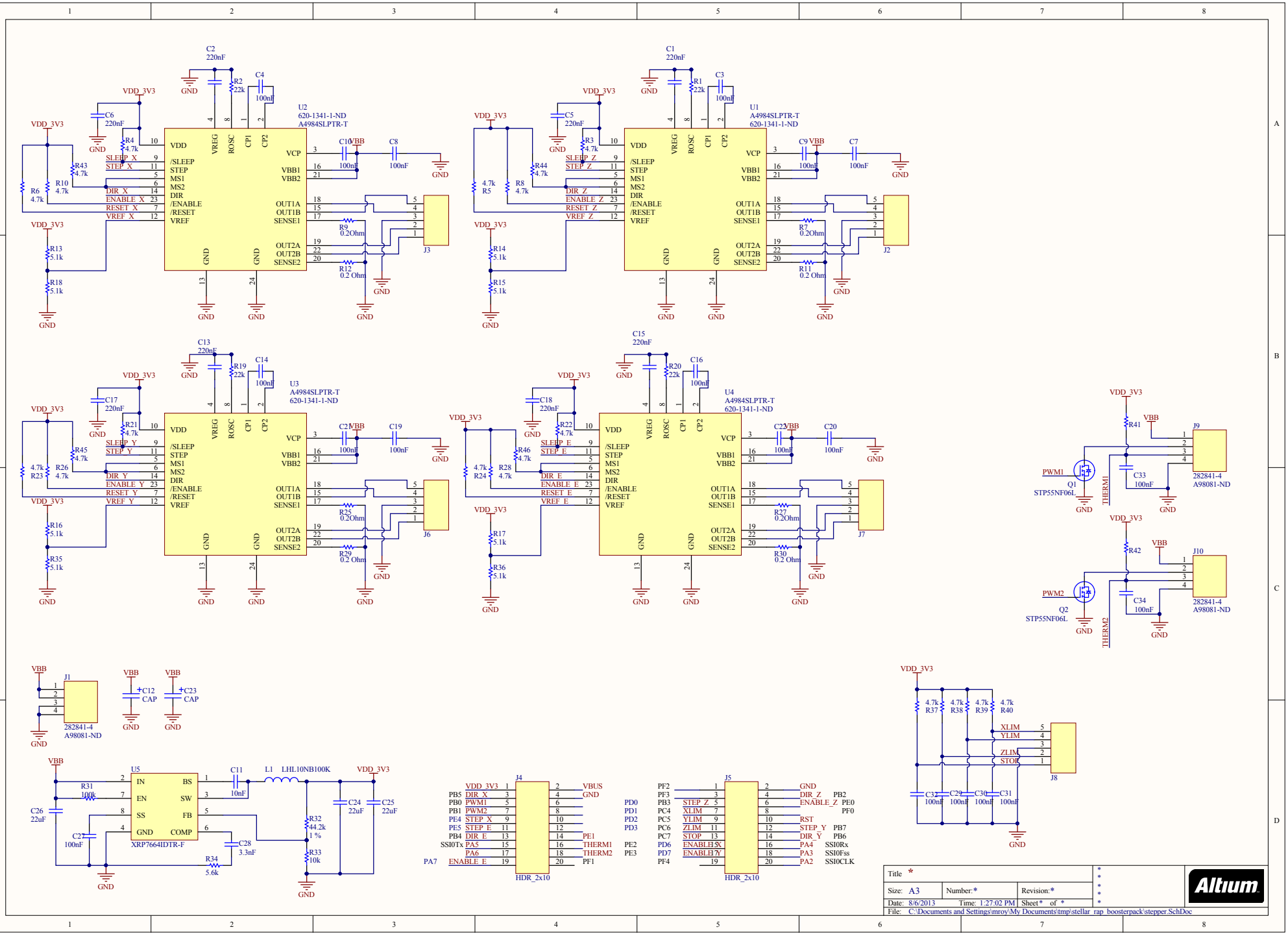
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STEP
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MS2
DIR
/ENABLE
/RESET
VREF
VREG
ROSC
CP1
CP2
VCP
VBB1
VBB2
OUT1A
OUT1B
SENSE1
OUT2A
OUT2B
SENSE2
GND
VDD_3V3
VBB
J1
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A98081-ND
J2
J3
J6
J7
J8
J9
J10
Q1
STP55NF06L
Q2
STP55NF06L
PWM1
PWM2
THERM1
THERM2
U5
XRP7664IDTR-F
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EN
SS
GND
BS
SW
FB
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Altium